

10/24/68

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

JC3
897

O.I.P.E. MA SCANNED <i>KSD</i> o.a. <i>CS1</i>	PATENT DATE
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APPLICATION NO. 09/816926	CONT/PRIOR D F	CLASS 712	SUBCLASS 32	ART UNIT 2183	EXAMINER Chen, Eddie
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APPLICANTS

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Configurable hardware block

TITLE

PTO-2040
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
			_____ (Date)	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ _____ _____	_____ (Primary Examiner) (Date)			
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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